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(54) Dual Damascene process having tapered vias

A process for forming a dual-damascene interconnect employs a spun-on organic layer above an interlayer dielectric having a set of apertures for vias that forms tapered regions about the apertures without penetrating the apertures. The slope of the tapered regions is transferred during the etching process to form selfaligned tapered vias. A silicon substrate (10) over which an oxide or other insulating layer (110) is first formed. A layer first level of metal interconnect (120) and a layer of SiO₂ (130) are formed insequent onto the structure, then an aperture (140) extending down to and stopping on metal interconnect layer is formed. Now an anti-reflective coating (ARC) layer (135) is put down and spun onto the structure. Unexpectedly, the surface tension of the ARC layer prevents the ARC material from getting into the aperture but forms a tapered rim at its edge (as illustrated). After baking the ARC layer, a layer of resist (150) is deposited, exposed, and developed to form a novel aperture (142). An non-isotropic etch using CF₄/ O₂ chemistry as the etching gas is performed, then both the resist and ARC layer are stripped. The result is an aperture having a tapered bottom selection which, when filled with metal (147/147) and polished to become level with the top of SiO2 layer produces the chemical Damascene interconnect.

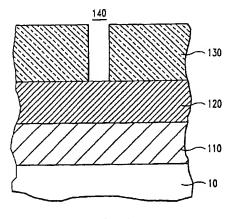
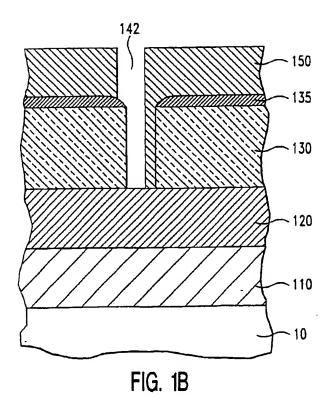
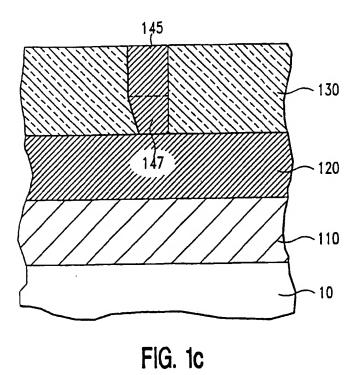


FIG. 1A







EUROPEAN SEARCH REPORT

Application Number EP 96 48 0062

DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document with indication, where appropriate, Releva				TACCIDICATION OF THE	
Category	Citation of document with i of relevant pa		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL6)	
A	PATENT ABSTRACTS OF vol. 010, no. 129 (& JP-A-60 261141 (F 1985, * abstract *	JAPAN E-403), 14 May 1986 OOMU KK), 24 December	1	H01L21/768	
A	March 1993 * column 1. line 45	- column 1, line 58 * - column 5, line 21 * ph 1; claims 1-15;	3,4		
D,A	US-A-4 461 672 (MUS 1984 * the wole document	SER MARY E) 24 July	1		
D,A	US-A-5 173 442 (CAR 1992 * the whole documen	EY DAVID H) 22 December	1		
				TECHNICAL FIELDS SEARCHED (IM.Cl.6)	
				HOIL	
	The present search report has b	een drawn up for all claims		<u> </u>	
Place of search Date of completion of the search				Examines	
THE HAGUE 9 Jan		9 January 1997	7 Königstein, C		
X: particularly relevant if taken alone after Y: particularly relevant if combined with another D: docu document of the same category L: docum A: technological background		E: earlier patent doc after the filing da ther D: document cited is L: document cited for dc: member of the sa	Inciple underlying the Invention nt document, but published on, or ing date ited in the application ited for other reasons the same patent family, corresponding		

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